

# Contents

<b>1. Introduction.....</b>	<b>1</b>
1.1 Background and Motivation .....	1
1.2 Terminology .....	3
1.2.1 Embedded Systems .....	3
1.2.2 Cores .....	4
1.2.3 System-on-Chip .....	5
1.2.4 Test and Check .....	6
1.2.5 Fault-Tolerance Objectives .....	7
1.2.6 Safety-Critical Embedded Systems.....	8
1.3 Publications .....	9
<b>2. Fault Models and Fault-Behavior of Processor Structures .....</b>	<b>13</b>
2.1 Fault Models.....	15
2.1.1 Permanent Faults .....	15
2.1.2 Transient Faults .....	17
2.2 Embedded Processor Architectures.....	19
2.2.1 Control and Data Path.....	19
2.2.2 Processor Types.....	21
2.2.3 Fault Effects in Processors.....	23
<b>3. On-line Check Technology for Processor Components .....</b>	<b>27</b>
3.1 State of the Art .....	27
3.2 Component On-line Check Using Extended Berger Code Prediction .....	29
3.2.1 BCP for Integer Data-Paths .....	29
3.2.2 BCP for Floating-Point Components .....	31
3.2.3 Results .....	37
3.3 Component On-line Check with Cross-Parity Check .....	39
3.3.1 Introduction.....	39
3.3.2 Cross-Parity Observation.....	42
3.3.3 Cross-Parity Error Detection Capabilities and Limitations.....	46
3.3.4 Results .....	52
<b>4. On-line Check Technology for Processor Control Signals .....</b>	<b>55</b>
4.1 State of the Art .....	55
4.2 Control-Signal On-line Check with Pseudo-TMR Controller .....	55
4.3 Control-Signal On-line Check with State Code Prediction .....	57
4.3.1 Introduction.....	57
4.3.2 Straightforward Processor State Encoding and Observation .....	58
4.3.3 Partitioned State Encoding and Observation.....	62
4.3.4 Outlook Regarding to Controller On-line Check .....	63

<b>5. Fast Processor Recover Techniques with Micro Rollback .....</b>	<b>65</b>
5.1 Previous Techniques and State of the Art .....	65
5.2 Micro Rollback with a Master-Trailer-Structure.....	67
5.2.1 Micro Rollback Test Circuit .....	67
5.2.2 Micro Rollback Technique for Simple Microprocessors .....	70
5.2.3 Results .....	74
5.3 Micro Rollback in Pipeline-Processors .....	75
5.3.1 Recover Techniques for a Pipeline Processor .....	75
5.3.2 Implementations and Results .....	78
<b>6. Conclusion and Outlook .....</b>	<b>81</b>
<b>Appendix – Demonstration Processors .....</b>	<b>87</b>
A.1 Microprocessor t4008.....	87
A.2 Microprocessors t5008/16/32x .....	96
A.3 Digital Signal Processors uDSP32a/b.....	105
A.4 Pipeline Processors DLX32/64fpu_p .....	112
<b>Abbreviations, Symbols and Identifiers.....</b>	<b>117</b>
<b>List of Figures .....</b>	<b>119</b>
<b>List of Tables .....</b>	<b>121</b>
<b>References.....</b>	<b>123</b>