

Table of Contents

Invited Papers

From Software to Hardware and Back	1
<i>Paul Feautrier</i>	
Of Elections and Electrons	3
<i>Peter Y. Ryan</i>	

Regular Papers

Formal Verification of an Avionics Sensor Voter Using SCADE	5
<i>Samar Dajani-Brown, Darren Cofer, and Amar Bouali</i>	
Mixed Delay and Threshold Voters in Critical Real-Time Systems	21
<i>Chiheb Kossentini and Paul Caspi</i>	
Towards a Methodological Approach to Specification and Analysis of Dependable Automation Systems	36
<i>Simona Bernardi, Susanna Donatelli, and Giovanna Dondossola</i>	
On Two-Sided Approximate Model-Checking: Problem Formulation and Solution via Finite Topologies	52
<i>Jennifer M. Davoren, Thomas Moor, R.P. Goré, Vaughan Coulthard, and Anil Nerode</i>	
On Timed Automata with Input-Determined Guards	68
<i>Deepak D'Souza and Nicolas Tabareau</i>	
Decomposing Verification of Timed I/O Automata	84
<i>Dilsun Kirli Kaynar and Nancy Lynch</i>	
Symbolic Model Checking for Simply-Timed Systems	102
<i>Nicolas Markey and Philippe Schnoebelen</i>	
Robustness and Implementability of Timed Automata	118
<i>Martin De Wulf, Laurent Doyen, Nicolas Markey, and Jean-François Raskin</i>	
Real-Time Testing with Timed Automata Testers and Coverage Criteria ..	134
<i>Moez Krichen and Stavros Tripakis</i>	
Monitoring Temporal Properties of Continuous Signals	152
<i>Oded Maler and Dejan Nickovic</i>	

A Unified Fault-Tolerance Protocol	167
<i>Paul Miner, Alfons Geser, Lee Pike, and Jeffrey Maddalon</i>	
Automating the Addition of Fail-Safe Fault-Tolerance: Beyond Fusion-Closed Specifications	183
<i>Felix C. Gärtner and Arshad Jhumka</i>	
Modeling and Verification of a Fault-Tolerant Real-Time Startup Protocol Using Calendar Automata	199
<i>Bruno Dutertre and Maria Sorea</i>	
Static Fault-Tolerant Real-Time Scheduling with “Pseudo-topological” Orders	215
<i>Cătălin Dima, Alain Girault, and Yves Sorel</i>	
The Influence of Durational Actions on Time Equivalences	231
<i>Harald Fecher</i>	
Bounded Model Checking for Region Automata	246
<i>Fang Yu, Bow-Yaw Wang, and Yao-Wen Huang</i>	
Some Progress in Satisfiability Checking for Difference Logic	263
<i>Scott Cotton, Eugene Asarin, Oded Maler, and Peter Niebert</i>	
Model-Checking for Weighted Timed Automata	277
<i>Thomas Brihaye, Véronique Bruyère, and Jean-François Raskin</i>	
Symbolic Model Checking for Probabilistic Timed Automata	293
<i>Marta Kwiatkowska, Gethin Norman, Jeremy Sproston, and Fuzhi Wang</i>	
Structured Modeling of Concurrent Stochastic Hybrid Systems	309
<i>Mikhail Bernadsky, Raman Sharykin, and Rajeev Alur</i>	
Computing Schedules for Multithreaded Real-Time Programs Using Geometry	325
<i>Philippe Gerner and Thao Dang</i>	
Forward Reachability Analysis of Timed Petri Nets	343
<i>Parosh Aziz Abdulla, Johann Deneux, Pritha Mahata, and Aletta Nylén</i>	
Lazy Approximation for Dense Real-Time Systems	363
<i>Maria Sorea</i>	
Learning of Event-Recording Automata	379
<i>Olga Grinchtein, Bengt Jonsson, and Martin Leucker</i>	
Author Index	397