

Table of Contents

Keynote Address

Approaches to Quality of Service in High-Performance Networks

A. A. Chien

University of Illinois and Hewlett Packard Laboratories, USA1

Session I Routing I

Session Chair: R. Bopanna

University of Texas at San Antonio, US

Integrated Multi-class Routing

M. L. Fulgham and L. Snyder

University of Washington, USA21

Congestion Control in the Wormhole-Routed Torus with Clustering and Delayed Deflection

C. Hyatt and D. P. Agrawal

North Carolina State University, USA.....33

Multicasting in Irregular Networks with Cut-Through Switches Using Tree-Based Multidestination Worms

R. Sivaram, D. K. Panda and C. B. Stunkel

Ohio State University, USA and IBM T. J. Watson Research Center, USA39

Poster Session

CCSIMD: A Concurrent Communication and Computation Framework for SIMD Machines

V. Garg and D. E. Schimmel

Georgia Institute of Technology, USA.....55

Arctic Switch Fabric

G. A. Boughton

Massachusetts Institute of Technology, USA65

Session II	<i>Router and Network Architectures I</i>	
Session Chair:	K. Bolding	
	Seattle Pacific University, USA	
<i>STREAMER: Hardware Support for Smoothed Transmission of Stored Video over ATM</i>		
S.-W. Moon, P. Pillai and K. G. Shin		
University of Michigan, USA.....	75	
<i>Preliminary Evaluation of a Hybrid Deterministic/Adaptive Router</i>		
D. Miller and W. A. Najjar		
Colorado State University, USA.....	89	
<i>HiPER-P: An Efficient, High-Performance Router for Multicomputer Interconnection Networks</i>		
P. May, S. M. Chai and D. S. Wills		
Georgia Institute of Technology, USA.....	103	
Session III	<i>Router and Network Architectures II (Invited Presentations)</i>	
Session Chair:	D. Pander	
	Ohio State University, USA	
<i>ServerNetTM II</i>		
D. Garcia and W. Watson		
Tandem Computers Inc., USA	119	
<i>Embedded Systems Standards</i>		
C. Lund		
Mercury Computer Systems, USA	137	
<i>Challenges in the Design of Contemporary Routers</i>		
C. B. Stunkel		
IBM T. J. Watson Research Center, USA	139	
Panel Session	153	
Moderator:	Dhabaleswar Panda	
	Ohio State University, USA	
Panelists:	Andrew Chien, University of Illinois and Hewlett Packard, USA	
	Al Davis, University of Utah, USA	
	Thorsten von Eicken, Cornell University, USA	
	Dave Garcia, Tandem Computers, USA	
	Craig Stunkel, IBM T. J. Watson Research Center, USA	

Session IV *Messaging Layer Support*

D. E. Schimmel

Georgia Institute of Technology, USA

Evaluation of Communication Mechanisms in Invalidate-Based Shared Memory Multiprocessors

G. T. Byrd and M. J. Flynn

Stanford University, USA 159

How Can We Design Better Networks for DSM Systems?

D. Dai and D. K. Panda

Ohio State University, USA 171

Integration of U-Net into Windows/NT (Invited Presentation)

T. von Eicken

Cornell University, USA 185

Session V *Routing II*

Session Chair: T. Pinkston

University of Southern California, USA

Distance-Based Flow Control in Wormhole Networks

A.-H. Smai and L.-E. Thorelli

Royal Institute of Technology, Sweden 189

On the Use of Virtual Channels in Networks of Workstations with Irregular Topology

F. Silla and J. Duato

Universidad Politécnica de Valencia, Spain 203

Multicasting on Switch-Based Irregular Networks Using Multi-Drop Path-Based Multidestination Worms

R. Kesavan and D. K. Panda

Ohio State University, USA 217

Power/Performance Trade-offs for Direct Networks

C. S. Patel, S. M. Chai, S. Yalamanchili and D. E. Schimmel

Georgia Institute of Technology, USA 231

Session VI Router and Network Architectures III

Session Chair: J. Duato

Universidad Politécnica de Valencia, Spain

ChaosLAN: Design and Implementation of a Gigabit LAN Using Chaotic Routing

N. R. McKenzie, K. Bolding, C. Ebeling and L. Snyder

Mitsubishi Electric Research Laboratory, Seattle Pacific University

University of Washington, USA 247

Does Time-Division Multiplexing Close the Gap Between Memory and Optical Communication Speeds?

X. Yuan, R. Gupta and R. Melhem

University of Pittsburgh, USA 261

Session VII Deadlock Issues

Session Chair: S. Yalamanchili

Georgia Institute of Technology, USA

Modeling Message Blocking and Deadlock in Interconnection Networks

S. Warnakulasuriya and T. M. Pinkston

University of Southern California, USA 275

On the Reduction of Deadlock Frequency by Limiting Message Injection in Wormhole Networks

P. López, J. M. Martínez, J. Duato and F. Petrini

Universidad Politécnica de Valencia, Spain and Università di Pisa, Italy 295

Author's Index 309