

Table of Contents

Invited Papers

- High Level System Design and Analysis Using Abstract State Machines . . . 1
Egon Börger
- Enriching the Software Development Process by Formal Methods 44
Manfred Broy, Oscar Slotosch

Regular Papers

- Formal Program Development in Geometric Modeling 62
François Puitg, Jean-François Dufourd
- Design of Distributed Multimedia Applications (DAMD) 77
Wanderley Lopes de Souza, Paulo Nazareno Maia Sampaio, Jean-Marie Farines, Roberto Milton Scheffel, Maria Janilce B. Almeida, Luciano Paschoal Gaspar, Lissandro Zambenodetti Granville, Roberto Willrich, Murilo S. de Camargo, Marcelo Domingos
- Structured Formal Verification of a Fragment of the IBM S/390 Clock Chip 92
Alfons Geser, Wolfgang Kuchlin
- Automated Test Set Generation for Statecharts 107
K. Bogdanov, M. Holcombe, H. Singh
- Rigorous Compiler Implementation Correctness: How to Prove the Real Thing Correct 122
Wolfgang Goerigk, Ulrich Hoffmann
- Translation Validation: From DC+ to C 137
A. Pnueli, O. Shtrichman, M. Siegel
- A Practical Hierarchical Design by Timed Simulation Relations for Real-Time Systems 151
Satoshi Yamane
- A Lightweight Approach to Formal Methods 168
Sten Agerholm, Peter Gorm Larsen
- An Open Environment for the Integration of Heterogeneous Modelling Techniques and Tools 184
Robert Büssow, Wolfgang Grieskamp, Winfried Heicking, Stephan Herrmann

Integrating Domain Specific Language Design in the Software Life Cycle	196
<i>Philipp W. Kutter, Daniel Schweizer, Lothar Thiele</i>	
Flexible and Reliable Process Model Properties: An Integrated Approach	213
<i>Tiziana Margaria, Volker Gruhn</i>	
A Symbolic Model Checker for ACTL	228
<i>A. Fantechi, S. Gnesi, F. Mazzanti, R. Pugliese, E. Tronci</i>	
Critical Systems Validation and Verification with CSP and FDR	243
<i>Michael Goldsmith, Irfan Zakiuddin</i>	
UniForM Perspectives for Formal Methods	251
<i>Bernd Krieg-Brückner</i>	
The UniForM WorkBench - A Higher Order Tool Integration Framework	266
<i>Einar W. Karlsen</i>	

Application Papers

Two Real Formal Verification Experiences: ATM Switch Chip and Parallel Cache Protocol	281
<i>Masahiro Fujita, Sree P. Rajan, Alan Hu</i>	
Formal Methods in the Specification of the Emergency Closing System of the Eastern Scheldt Storm Surge Barrier	296
<i>Meine van der Meulen, Tim Clement</i>	
The New Topicality of Using Formal Models of Security Policy within the Security Engineering Process	302
<i>Frank Koob, Markus Ullmann, Stefan Wittmann</i>	

Tool Papers

Towards Comprehensive Tool Support for Abstract State Machines: The ASM Workbench Tool Environment and Architecture	311
<i>Giuseppe Del Castillo</i>	
The IFAD VDM Tools: Lightweight Formal Methods	326
<i>Sten Agerholm, Peter Gorm Larsen</i>	
KIV 3.0 for Provably Correct Systems	330
<i>Michael Balser, Wolfgang Reif, Gerhard Schellhorn, Kurt Stenzel</i>	
PVS: An Experience Report	338
<i>S. Owre, J. M. Rushby, N. Shankar, and D. W. J. Stringer-Calvert</i>	
QUEST: Overview over the Project	346
<i>Oscar Slotosch</i>	

VSE: Controlling the Complexity in Formal Software Developments 351
*Dieter Hutter, Heiko Mantel, Georg Rock, Werner Stephan, Andreas
Wolpers, Michael Balsler, Wolfgang Reif, Gerhard Schellhorn, Kurt
Stenzel*

The wHOLe System 359
Mark E. Woodcock

Z/EVES Version 1.5: An Overview 367
ORA Canada

Author Index 377