

# List of Contents

<b>1 Introduction</b>	<b>1</b>
<b>Chris Toumazou, John Lidgey and David Haigh</b>	
1.1 Analogue IC Design	1
1.2 The Current-Mode Approach	1
1.3 Advances in Semiconductor Technology.	3
1.4 New Analogue Building-Blocks	5
1.5 Current-Mode Systems	5
1.6 Review of the Chapters	5
1.7 Future current-mode analogue IC Design	10
 <b>2 Current-mode Circuits From A Translinear Viewpoint A Tutorial</b>	<b>11</b>
<b>Barrie Gilbert</b>	
2.1 Introduction	11
2.2 General Principles	12
2.2.1 Translinear Loops and Networks	15
2.2.2 The Translinear Principle	17
2.2.3 Use of Emitter Area Ratios	20
2.2.4 VBE Mismatch	21
2.2.5 Some Examples of TL Analysis	22
2.2.6 The Ratiometric Viewpoint	26
2.2.7 Extension of TL Theory to Include Voltage Generators	26
2.2.8 Effect of Other Device Non-Idealities	28
2.3 Squaring And Square-rooting	32
2.3.1 Scaling	34
2.3.2 TL Squaring Circuits	36
2.3.3 RMS-DC Conversion	39
2.3.4 Two-Quadrant TL Squarers	44
2.3.5 Square-Rooting and Geometric-Mean	46
2.3.6 Vector Magnitude	47
2.4 Analog Multipliers And Dividers	53
2.4.1 Basic Four-Transistor Multiplier Cells	55
2.4.2 The Beta-Immune Type 'A' Cell.	56
2.4.3 The Beta-Allergic Type 'B' Cell	62
2.4.4 Distortion Mechanisms	64
2.4.5 The 'Log-Antilog' Multiplier	66
2.4.6 The Six-Transistor Multiplier Cell	69
2.5 The Translinear Cross-quad	70
2.5.1 Caprio's Quad	72
2.5.2 Linear Half-Wave Rectifier	73
2.5.3 PTAT (Delta-VBE) Cell	74
2.5.4 Another Wideband Squarer	77
2.5.5 Another Two-Quadrant Multiplier	79
2.6 Miscellaneous TL And TN Circuits	81
2.6.1 Signal Normalization	82
2.6.2 Minimum and Maximum Functions	84
2.6.3 Trigonometric Functions	86
2.7 Acknowledgements	90
2.8 References	90
2.9 Bibliography	91

<b>8 Gallium Arsenide Analogue Integrated Circuit Design Techniques</b>	<b>327</b>
<b>Chris Toumazou and David Haigh</b>	
8.1 Introduction	327
8.2 Technology, Devices and Simulation	328
8.3 Current Mirrors	330
8.3.1 Classification of Current Mirrors	330
8.3.2 Positive Non-inverting Current Mirror	331
8.3.3 Negative Inverting Voltage-following Current Mirror	332
8.3.4 Linear negative current mirror	340
8.3.5 Current mirror performance evaluation	341
8.4 Single-ended to Differential Converter	343
8.5 Differential to Single-ended Converter	344
8.6 Linear Transconductors	345
8.6.1 Linearisation Principles	345
8.6.2 Linear Transconductor Implementation	347
8.7 High Gain Operational Transconductance Amplifiers	350
8.7.1 General	350
8.7.2 Circuits Using Diode Level-shifting	350
8.7.3 Circuits Using Device Width Ratioing	352
8.7.4 Fast Settling Architectures	355
8.8 Buffer Amplifiers	358
8.9 Applications	359
8.9.1 Switched Capacitor Filter Example	359
8.9.2 Microwave Applications	363
8.9.3 Optical Applications	365
8.9.4 Continuous-time Filtering	366
8.10 Conclusions	367
8.11 Acknowledgements	367
8.12 References	367
<b>9 Continuous-Time Filters</b>	<b>371</b>
<b>Rolf Schaumann and Mehmet Ali Tan</b>	
9.1 Introduction	371
9.2 Design of Continuous-Time Transconductance-C Filters	374
9.2.1 Cascade Design	375
9.2.2 Element Substitution Method	377
9.2.3 The Operational Simulation Method	379
9.2.4 Effects of OTA Nonidealities	382
9.3 Tuning Procedures	383
9.4 Discussion and Summary	384
9.5 References	385
<b>10 Continuous-time and Switched Capacitor Monolithic Filters Based on LCR Filter Simulation using Current and Charge Variables</b>	<b>387</b>
<b>David Haigh</b>	
10.1 Introduction	387
10.2 LCR Prototype Filters and State Matrix Formulations	388
10.3 Signal Flow Graph Representations and Realisations	389
10.4 Integrator Subcomponents	392
10.5 Integrator Types	393
10.5.1 Voltage Integrators	393
10.5.2 Current Integrators	394
10.5.3 Mixed Variable Integrators	394
10.5.4 Integrator Canonicity	395
10.6 Signal Flow Graph Scaling	395
10.7 Generalised Filter Realisation Architecture	396

10.7.1 The Need for a Generalisation	396
10.7.2 Generalised Architecture	397
10.8 Sensitivity	399
10.9 General Scaling Operations	401
10.10 Transconductor Configurations	401
10.11 Classification of Filter Realisations and Properties	403
10.11.1 General	403
10.11.2 Row-type realisation	404
10.11.3 Column-type realisation	404
10.11.4 Non-canonic realisation	405
10.11.5 Cross-type simulation	405
10.11.6 'Z'-type simulation	405
10.11.7 Elliptic Filters	406
10.12 Examples	406
10.12.1 Polynomial Filters	406
10.12.2 Elliptic Filters	411
10.13 Conclusions	412
10.14 Acknowledgements	412
10.15 References	412
<b>11 Switched-Current Filters</b>	<b>415</b>
<b>John.B.Hughes</b>	
11.1 Introduction	415
11.2 Switched-capacitor Background	416
11.3 Switched-current Systems	421
11.4 Switched-current Integrators	423
11.4.1 Current Memories	423
11.4.2 First-generation Switched-current Integrators	424
11.4.3 Second-generation Switched-current Integrators	429
11.5 Switched-current Differentiators	435
11.6 Switched-current State-variable Filter Synthesis	438
11.6.1 Integrator-based Biquadratic Sections	438
11.6.2 Filter Design Example	439
11.6.3 Differentiator-based Biquadratic Section	441
11.7 Analogue Errors	443
11.8 Current Memory Circuit Enhancements	445
11.9 Interface Circuits	446
11.10 Conclusions	447
11.11 Acknowledgements	448
11.12 References	448
<b>12 Analog Interface Circuits For VLSI</b>	<b>451</b>
<b>Evert Seevinck</b>	
12.1 Introduction	451
12.2 Input interface circuits	453
12.2.1 Linear CMOS voltage-current converters	453
12.2.2 Large-signal bipolar V-I converter	457
12.2.3 High-frequency bipolar V-I converter	458
12.2.4 CMOS subnanosecond ECL-compatible input buffer	459
12.3 Monolithic filters	460
12.3.1 Bipolar video-frequency filters	462
12.3.2 CMOS VHF filters	466
12.3.3 Integration of large time-constants	468
12.4 Signal conditioning	468
12.5 Internal interfacing	472
12.5.1 Current sensing in SRAMs	473
12.5.2 Two-lead multiplexing system	475

12.6 Output interface circuits	476
12.6.1 Bipolar power amplifier	476
12.6.2 Bipolar power transistor design for extended safe operating area	478
12.6.3 CMOS subnanosecond true-ECL output buffer	479
12.7 Power supply interfacing	481
12.7.1 "Nanopower" CMOS voltage and current reference	482
12.7.2 Regulator circuit with adaptive biasing having low standby current	484
12.7.3 On-chip IDD monitor circuit	485
12.8 Conclusions	486
12.9 Acknowledgments	487
12.10 References	487
<b>13 Current Mode A/D and D/A Converters</b>	<b>491</b>
<b>C. Andre T. Salama, David G. Nairn and Henry W. Singor</b>	
13.1 Introduction	491
13.2 Analog to digital converters	491
13.2.1 Algorithmic analog-digital conversion	492
13.2.2 Basic mirror analog-to-digital converters	493
13.2.3 Active mirror analog-to-digital converters	496
13.2.4 Current matching analog-to-digital converters	499
13.2.5 Summary	502
13.3 Digital to Analog Converters	503
13.3.1 Split collector lateral bipolar transistors in CMOS	504
13.3.2 DAC Configuration	505
13.3.3 Weighted Current Source Implementation	507
13.3.4 High-Speed Current Switch Implementation	508
13.3.5 Control Amplifier Implementation	509
13.3.6 Experimental Results	510
13.3.7 Summary	511
13.4 Acknowledgements	512
13.5 References	512
<b>14 Applications of current-copier circuits</b>	<b>515</b>
<b>David Vallancourt and Steven J. Daubert</b>	
14.1 Introduction	515
14.2 Current Copier Concept Review	515
14.3 Applications of Current Copiers	517
14.4 Examples of Function Blocks	517
14.4.1 Ratio-Independent Integer Current Multipliers	517
14.4.2 A Ratio-Independent Voltage Multiplier/Divider [10]	518
14.4.3 A Ratio-Independent Current Divider [11]	520
14.5 Examples of Systems	523
14.5.1 A Self-Calibrating D/A Converter [2,9]	523
14.5.2 A Ratio-Independent Algorithmic A/D Converter [3]	526
14.5.3 A Pipelined A/D Converter Using Current Division [15]	527
14.5.4 A Sigma Delta Front End	531
14.5.5 Digitally Programmable Analog Filters [5,10 ]	531
14.6 References	532
<b>15 Integrated Current Conveyor</b>	<b>535</b>
<b>Doug C.Wadsworth</b>	
15.1 Introduction	535
15.1.1 Previous Conveyor Realizations	535
15.1.2 New Conveyor Realization	537
15.2 Topology Evolution	537
15.2.1 Basic New Topology	538
15.2.2 Improved Topology	541

15.2.3 Final Topology	541
15.3 Process Characteristics	545
15.3.1 Standard Bipolar Processing	545
15.3.2 Complementary Bipolar Processing	545
15.4 Circuit description	546
15.4.1 Unity Gain Buffers	547
15.4.2 Inverting CCI Configuration	548
15.4.3 Non-Inverting CCII Configuration	549
15.4.4 JFET Output Configuration	550
15.5 Performance	551
15.5.1 Conveyor Output Impedance	551
15.5.2 Conveyor Frequency Response	552
15.5.3 Conveyor Transient Response	552
15.5.4 Conveyor Harmonic Distortion	552
15.5.5 Conveyor Accuracy	554
15.5.6 Performance Summary	554
15.6 Applications	554
15.6.1 Professional Audio Electronics	556
15.6.2 Precision Rectification	558
15.6.3 Current Feedback Amplifier	562
15.7 Conclusions	565
15.8 References	565
<b>16 Applying "Current Feedback" to Voltage Amplifiers</b>	<b>569</b>
<b>Derek F. Bowers</b>	
16.1 Introduction	569
16.2 The Operational Amplifier	569
16.3 The Instrumentation Amplifier	573
16.4 The 'Current Feedback' Operational Amplifier	574
16.5 Large Signal Effects on the Current Feedback Op-Amp	579
16.6 Drawbacks of the Current Feedback Op-Amp	579
16.7 Design Techniques for Current Feedback Op-Amps	581
16.8 Practical Current Feedback Op-Amp Designs	583
16.9 'Current Feedback' Applied to Instrumentation Amplifiers.	585
16.10 'Active feedback' applied to instrumentation amplifiers	588
16.11 Typical Current and Active Feedback Instrumentation Amplifiers	592
16.12 Conclusion	593
16.13 Acknowledgment	594
16.14 References.	594
<b>17 Neural Network Building Blocks for Analog MOS VLSI</b>	<b>597</b>
<b>Steven Bibyk and Mohammed Ismail</b>	
17.1 Neural Networks and Analog VLSI	597
17.2 Neural Models	598
17.3 Synaptic Weight Storage	600
17.3.1 Nonvolatile Floating-gate Storage	600
17.4 Current Mode, Neural Network Building Blocks	602
17.4.1 Programmable Synaptic Element	603
17.4.2 Inner Product Circuits.	605
17.4.3 Current Thresholding	607
17.4.4 Silicon Implementation	609
17.5 Adaptive Filtering and Applications	610
17.6 Conclusions	612
17.7 Acknowledgements	613
17.8 References	613

<b>18 Future of Analogue Integrated Circuit Design</b>	<b>617</b>
<b>Phillip E. Allen</b>	
18.1 Present Status of Analogue Integrated Circuit Design	617
18.2 Technological Trends	620
18.3 Design Trends	623
18.4 Simulation and Modeling Trends	627
18.5 Design Automation	631
18.6 Testing Trends	635
18.7 Future Trends and Expectations	637
18.8 References	639
<b>Index</b>	<b>641</b>